

PATENT NUMBER

**U.S. UTILITY Patent Application**

TO	O.I.P.E.	PATENT DATE
SCANNED	K52 Q.A.	

APPLICATION NO. 09/815312	CONT/PRIOR.	CLASS 705	SUBCLASS 17	ART UNIT <del>3621</del>	EXAMINER Backer
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## APPLICANTS

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**TITLE**

System and method for low-latency processing of network communications and computer circuits

PTO-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
Amount Due			Date Paid	
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